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U.S. UTILITY Patent Application

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PATENT NUMBER and  
ISSUE DATE

APPL NUM 10023849	FILING DATE 12/21/2001	CLASS 257 288	SUBCLASS 288	GAU 2822	EXAMINER Rose
<b>**APPLICANTS:</b> Yagisnita Atsushi; Matsuo Kouji; Akasaka Yasushi; Suguro Kyoichi; Tsunashima Yoshitaka;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/105,960 06/29/1993					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 9-174195 06/30/1997					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
verified and Acknowledged Examiners's initials				04329.1949-01000	
TITLE : Semiconductor device					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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